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Title of Invention:	METHOD AND SYSTEM FOR FACILITATING ELECTRONIC CIRCUIT AND CHIP DESIGN USING REMOTELY LOCATED RESOURCES			
First Named Inventor/Applicant Name:	William H. Robertson			
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1 Issue Fee Payment (PTO-85B)	IssueFeeTransmittal.pdf	101378	no	1	
		518f6178290ed3511551c28ab84cc1b9 e15c5e16			
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Amendment after Notice of Allowance	AmendmentAfterAllowance_	459160	no	13	
_	(Rule 312)	020508.pdf	0637ecc2b036378d5690c5f52f59578c2 a8be092	110	13
Warnings:					
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3 Fee Worksheet (PTO-06)	fee-info.pdf	8527	no	2	
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